

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S15 7	668	(IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem or driver)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:25
S15 8	0	(IC or (integrated adj1 circuit)) and configuration adj1 memory adj1 cell and loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:26
S15 9	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP) and configuration adj1 memory adj1 cell and loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:28
S16 0	0	configuration adj1 memory adj1 cell and loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:27
S16 1	2905	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP) and loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:08
S16 2	64	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP) with loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:32
S16 3	3	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP) and configuration with memory with cell and loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:28
S16 4	51	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC) with loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:10
S16 5	13	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC) with loss with (sync or synchroni\$6) and ((receiver and transmitter) or transceiver or sender)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:33
S16 6	264	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor) with loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:11

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S16 7	27	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor) with loss with (sync or synchroni\$6) and CRC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:11
S16 8	18	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor) with loss with (sync or synchroni\$6) and CRC and ((receiver and transmitter) or transceiver or sender)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:11
S16 9	29	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) and configuration adj1 memory adj1 cell with (transceiver or receiver or transmitter or modem or driver)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:02
S17 0	46	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or receiver or transmitter or modem or driver) with CRC with generat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:37
S17 1	1425	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or receiver or transmitter or modem or driver) and CRC with generat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:35
S17 2	19	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or receiver or transmitter or modem or driver) with CRC adj2 generat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:58
S17 3	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or receiver or transmitter or modem or driver) with CRC adj2 generat\$3 with loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:58
S17 4	23	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or receiver or transmitter or modem or driver) with loss with (sync or synchroni\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:58
S17 5	2	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or receiver or transmitter or modem or driver) with loss with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 11:59
S17 6	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) and configuration adj1 memory adj1 cell with (transceiver or receiver or transmitter or modem or driver) and loss with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:04

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S17 7	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) and configuration adj1 memory adj1 cell with (transceiver or receiver or transmitter or modem or driver) and (loss or out or off) with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:05
S17 8	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) and configuration adj1 memory with cell with (transceiver or receiver or transmitter or modem or driver) and (loss or out or off) with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:05
S17 9	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) and configuration adj1 memory with cell same (transceiver or receiver or transmitter or modem or driver) and (loss or out or off) with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:09
S18 0	0	configuration adj1 memory with cell same (transceiver or receiver or transmitter or modem or driver) and (loss or out or off) with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:06
S18 1	2	configuration adj1 memory with cell same (transceiver or receiver or transmitter or modem or driver) and (loss or out or off) with (sync or synchroni\$6) same detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:06
S18 2	10	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or receiver or transmitter or modem or driver) with (loss or out or off) with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:04
S18 3	3	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver or (receiver and transmitter) or modem) with (loss or out or off) with (sync or synchroni\$6) with detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 12:52
S18 4	1	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) with (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) with (loss or out or off) adj2 (sync or synchroni\$6) with detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:06
S18 5	1	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or core or processor or UART) same (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) with (loss or out or off) adj2 (sync or synchroni\$6) with detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:07
S18 6	180	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART) with ((out or off or loss) adj2 (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:17

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S18 7	1	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART) with ((out or off or loss) adj2 (sync or synchroni\$6)) and configuration adj1 memory with cell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:10
S18 8	829	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART) and ((out or off or loss) adj2 (sync or synchroni\$6)) and transceiver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:17
S18 9	7	"341"/.ccls. and (IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART) and ((out or off or loss) adj2 (sync or synchroni\$6)) and transceiver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:24
S19 0	13	"341"/.ccls. and (IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and ((out or off or loss) adj2 (sync or synchroni\$6)) and transceiver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:24
S19 1	14	"341"/.ccls. and (IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and ((out or off or loss) adj2 (sync or synchroni\$6)) and transceiver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 14:26
S19 2	1	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell with (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:05
S19 3	161	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 16:05
S19 4	56	(transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) with (loss or out or off) adj2 (sync or synchroni\$6) with detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:47
S19 5	0	S193 and S194	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:07
S19 6	587	(transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) and (loss or out or off) adj2 (sync or synchroni\$6) with detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:08

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S19 7	0	S193 and S196	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:08
S19 8	2	(transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) with (loss or out or off) adj2 (sync or synchroni\$6) adj2 detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:12
S19 9	3	"6091962"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:13
S20 0	2	"6091962" and detector with synchroni\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:13
S20 1	2	"6091962" and detector with synchroni\$6 and transceiver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:14
S20 2	99	(transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) same (loss or out or off) adj2 (sync or synchroni\$6) with detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:55
S20 7	17	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) and encoder	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 16:09
S20 8	8	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) and encoder with control\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 16:07